

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1-16. (Canceled)

17. (Currently Amended) ~~Integrated circuit according to claim 16,~~ An integrated circuit comprising at least a digital part comprising a plurality of transistors connected to one another so as to form a plurality of functional elements, the functional elements being grouped in subassemblies each comprising a first and a second electrical supply terminal and a clock input, the subassemblies being connected in series by means of their supply terminals to the terminals of a voltage supply source,

wherein a same clock signal is applied to the clock input of all subassemblies, by means of a device for shifting the levels of the clock signal, and

wherein the subassemblies are formed in such a way that the same current flows through the different subassemblies.

18. (Currently Amended) Integrated circuit according to ~~claim 16,~~ claim 17, wherein the clock inputs of at least two adjacent subassemblies are connected by a device for shifting the clock signal levels.

19. (Previously Presented) Integrated circuit according to claim 18, wherein the clock input of one of the end subassemblies is connected by means of an additional device for shifting the clock signal levels at the output of the clock circuit.

20. (Currently Amended) Integrated circuit according to any ~~claim 16,~~ claim 17, wherein the device for shifting the clock signal levels comprises at least one capacitor.

21. (Currently Amended) Integrated circuit according to ~~claim 16,~~ claim 17, wherein the device for shifting the clock signal levels comprises at least one transistor.

22. (Currently Amended) Integrated circuit according to ~~claim 16~~, claim 17, wherein all the subassemblies are identical.
23. (Currently Amended) Integrated circuit according to ~~claim 16~~, claim 17, wherein each of the subassemblies comprises a voltage limiting circuit connected between its power supply terminals.
24. (Previously Presented) Integrated circuit according to claim 23, wherein the voltage limiting circuit comprises a diode.
25. (Previously Presented) Integrated circuit according to claim 23, wherein the voltage limiting circuit comprises a transistor.
26. (Currently Amended) Integrated circuit according to ~~claim 16~~, claim 17, wherein each subassembly comprises a decoupling capacitor connected between the first power supply terminal and the second power supply terminal of the subassembly.
27. (Currently Amended) Integrated circuit according to ~~claim 16~~, claim 17, wherein the integrated circuit comprises means for electrical insulation between the subassemblies.
28. (Previously Presented) Integrated circuit according to claim 27, wherein the means for electrical insulation between the different subassemblies are reverse biased diode junctions.
29. (Previously Presented) Integrated circuit according to claim 27, wherein the means for electrical insulation between the different subassemblies are dielectric zones.
30. (Currently Amended) Integrated circuit according to ~~claim 16~~, claim 17, wherein the integrated circuit comprises silicon blocks achieved from a silicon-on-insulator substrate.